Effective reduction of cryptographic protocols specification for model-checking with Spin

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Abstract

In this article a practical application of the Spin model checker for verifying cryptographic protocols was shown. An efficient framework for specifying a minimized protocol model while retaining its functionality was described. Requirements for such a model were discussed, such as powerful adversary, multiple protocol runs and a way of specifying validated properties as formulas in temporal logic.

1. Introduction

A flaw in a cryptographic protocol may become a real security thread [1, 2]. Even a seemingly small protocol may produce a great number of possible behaviours. One of the methods to formally consider protocols correctness is model checking by representing the protocol as Büchi automata $M$, specifying every checked property as an LTL temporal formula $\alpha$ and checking satisfiability of the formula in the model $M \models \alpha$ [3, 4, 5, 6].

The automata of the protocol is typically generated from a more high-level description. This article has its focus on representing protocol models in the

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Promela language, which is an input for model checker Spin [7]. Choice of that tool was due to its effective, automatic minimizing techniques and its widespread use in the area of verification (see annual workshops page [8]).

Some examples of verifying cryptographic protocols with Spin can be found in the literature [9, 10, 11, 12, 13, 14, 15]. However, the presented models are too simple, not taking into consideration multiple runs or limiting the protocol attacker abilities. Most importantly they create a large automaton, even though not so complicated Needham–Schroeder protocol is considered. Such an approach for modelling a more complex protocol like JFK would result in a model not feasible to verify. Model publicized in [10] seems to be the most sophisticated, as it is scalable and includes parallel runs but it contains many redundant transition causing state-space explosion. Another approach presented in [15] uses interesting recursive structures but at the expense of great memory cost. This does not disable the possibility of finding the attacks but only full coverage of reachable states can assure the model behaviour correctness.

Also none of the mentioned models supports creating a readable counterexample indicating an attack. In this article a method for developing cryptographic protocol models, avoiding those drawbacks is outlined. The description of protocol framework is illustrated with the fragments of Promela code.

2. Problem Definition

Key establishment and authentication cryptographic protocols, such as Needham–Schroeder or JFK, can be modelled as automata so that their properties, described as temporal formulas, can be checked. The main problem is to keep such a model effectively verifiable. The satisfiability of the formulas in the model should increase confidence in the security of the protocols. Thus it is crucial to explicitly list requirements such a model must comply with. The environment in which the protocol is studied is considered an important matter [16, 17, 18]. The main points are the following:

: **Legal users** - can participate in parallel protocols taking different roles (initiator, responder). They can establish a session with other users including the intruder, that has a certificate like other legitimate users.

: **The intruder** - can at any point eavesdrop a message, alter it and resend it to another user in another protocol run. The adversary produces messages on the basis of his actual knowledge, creating new complex elements (e.g. encryptions) or resending the remembered ones.

: **Model scalability** - concerns the number of protocol runs and the attackers knowledge database.
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: **Additional data** - the information required for logical assertions that are written to check protocol properties must be stored. Also additional informations about protocol state are printed out and used later while producing a counterexample.

: **Model configuration** - description of a particular model configuration, should specify any constraints in the way that the messages are sent from the user to the user and the roles the users can take.

These specifications are responsible for models proper behaviour. While the above constraints hold, one important parameter must be minimized:

: **Model size** - affects the amount of memory and time needed for verification. Considering the exponential complexity of the model checking problem ($O(\#(M)) = O(2^{\#(P)})$, where $P$ is the number of atomic prepositions describing the states of model $M$ [19]), this seems to be a critical issue in practical applications.

3. Representing Protocol as an Automaton

To illustrate the idea of modelling protocols as $B\ddot{u}chi$ automata, an example is given in this section, showing a path from a protocol description up to the automaton. A clear, simple protocol is used (Fig. 1). Also no reduction techniques have been demonstrated yet. This keeps the model comprehensible so that the reader can understand the general methodology. The verification process consists of the following steps.

1. **Modelling protocol** - the verifier describes in the *Promela* language the behaviours of the protocol users and all the possible actions the adversary can take. A sample code representing the responder in the example protocol is shown in Fig. 1.

2. **Protocol as automaton** - the *Promela* code describes an automaton. A guard and an action are associated with every transition from state to state. In the automaton in Table 1 and Fig. 2, the state when the key is established can be reached only if the guard corresponding to signature correctness holds. The actions can change the variables values and message channels contents.

3. **Kripke structure** - incorporating variables values into automaton produces a *Kripke* structure [4, 6]. Here every state represents a possible configuration of variables values. The example structure is shown in Fig. 3.

4. **B\ddot{u}chi automaton** - nondeterminism of $B\ddot{u}chi$ automaton is crucial for model checking, as every possible path in the protocol must be analyzed. $B\ddot{u}chi$ automaton can be constructed from *Kripke* structure...
by copying the state labels onto the outgoing arcs [6], which can be seen in Fig. 4.

(5) The verified property - all the desirable properties of the protocol are written down as LTL logic formulas. The formulas contain references to variables from the protocol model. Each formula is negated to denote the unsafe states and automatically transformed into special never process in the Promela code with Spin or another tool [20], as shown in Fig. 5. This code can be also transformed into the Büchi automaton. Locations represented as double framed circles are accepting locations. The automaton accepts an infinite input if it makes the automaton visits accepting states infinitely often [5, 6].

(6) Verification algorithm - at the end an asynchronous product of all automata representing protocol users is constructed. This automaton is used to construct a synchronous product with the formula automaton [6]. The algorithm is to search the resulting automaton for a path that would traverse infinitely often through the formula automaton accepting locations [4, 6].

(7) Counterexample - such a path indicates an error in the protocol and presents a way an unsafe state can be reached. On the whole, the protocol is flawed if its model can produce a path, that is accepted by the automaton representing an undesirable situation.

The human verifier takes part only in the stages involving modelling the protocol in the Promela language and writing LTL logic formulas. Other activities are done automatically by the model checker tool. Actually effective implementations merge the described stages to reduce computing costs.

Table 1. Table with automaton describing responders states while participating in the protocol from Fig. 1.

<table>
<thead>
<tr>
<th>Transition</th>
<th>Current state</th>
<th>Gard</th>
<th>Transition effect</th>
<th>Next state</th>
</tr>
</thead>
<tbody>
<tr>
<td>t43</td>
<td>0</td>
<td>-</td>
<td>-</td>
<td>1</td>
</tr>
<tr>
<td>t44</td>
<td>1</td>
<td>-</td>
<td>m1?certi,expi</td>
<td>2</td>
</tr>
<tr>
<td>t45</td>
<td>2</td>
<td>-</td>
<td>printf('MSC: MSG2 Bob...')</td>
<td>3</td>
</tr>
<tr>
<td>t46</td>
<td>3</td>
<td>-</td>
<td>m2!B, ExpB, BPr, expi, ExpB</td>
<td>4</td>
</tr>
<tr>
<td>t47</td>
<td>4</td>
<td>-</td>
<td>m3?sigkey,sigexpi,sigexpr</td>
<td>7</td>
</tr>
<tr>
<td>t48</td>
<td>7</td>
<td>(( certi == A ...)</td>
<td>-</td>
<td>6</td>
</tr>
<tr>
<td>t49</td>
<td>7</td>
<td>(( certi != A ...)</td>
<td>-</td>
<td>13</td>
</tr>
<tr>
<td>t1</td>
<td>6</td>
<td>-</td>
<td>skip</td>
<td>11</td>
</tr>
<tr>
<td>t50</td>
<td>11</td>
<td>-</td>
<td>d_step{...}</td>
<td>12</td>
</tr>
<tr>
<td>t51</td>
<td>12</td>
<td>-</td>
<td>-</td>
<td>13</td>
</tr>
</tbody>
</table>
Modelling protocol

MSG1 A → B : A, k_a
MSG2 B → A : B, k_b, SIG{BPr}(k_a, k_b)
MSG3 A → B : SIG{APr}(k_a, k_b)

k_a = g^a mod p
k_b = g^b mod p
Key = k_b^a mod p = k_a^b mod p

/* responder */
proctype Bob(chan ml, m2, m3)
{
    byte exp1; /* D-H exponent of initiator */
    byte certi;/* certificate of initiator */
    byte sigkey, sigexpi, sigexpr; /* for values from sig */
    MSG1: ml?certi, expi;
    MSG2: printf("MSG2 Bob %d, %d, Sig %d(%d, %d)\n", B, ExpB, BPr, expi, ExpB);
        m2!B, ExpB, BPr, expi, ExpB;
    MSG3: m3?sigkey, sigexpi, sigexpr; /* sig */
        if /* check sig */
            ::(( (certi == A && sigkey == APr)
                 || (certi == E && sigkey == EPr)
                && sigexpi == expi && sigexpr == ExpB )
                -> skip;
        fi;
    FINISH: d_step( /* remember the data for verification in global variables */
        expbl = expi;
        certInit = certi;
    )
}

Fig. 1. Description of examplary key establishment cryptographic protocol based on the Diffie–Hellman schema and signature and the Promela code representing the responder.

4. Our approach

The most intuitive way to model protocol is to represent users as independent processes, sending messages through channels controlled by the intruder.
Unfortunately, such a model, though properly describing the protocol, might be too large to analyze. Due to exponential complexity of the problem [19], every redundancy in the model is expensive by means of memory and computation time.

So the ability to model a protocol is not sufficient for practical verification. Thus the constructions below were used in the presented model to reduce its complexity, while giving the intruder strong abilities.
The verified property

```c
#define successAB (exp2a == ExpB && exp1b == ExpA)
#define id_misbind (certInit == E && successAB)
never {
  T0_init:
    if
      :: ((id_misbind) -> goto accept_all)
    :: (i) -> goto T0_init
  fi;
  accept_all:
    skip
}
```

Fig. 5. Büchi automaton constructed from the LTL logic formula $\Diamond id\_misb$ (identity misbinding attack is possible). Specification in the Promela code (left) and graph representation (right).

4.1. Remembering Simple Message Elements

Simple elements known by the intruder are remembered as bytes in the EveDB array. Every element has unique value and can be accessed with a combination of defined indices. The values for the JFKi protocol are shown in the left column of Fig. 6. The example of access to elements can be found in the right column of Fig. 6. For instance the index of responders nonce nonr, is a sum of index indicating user identifier, nonce type and current protocol run ($otherUser + NONCE + comm$). On the other hand, exponentials are reused between protocol runs so to access them the comm variable indicating the run is not used. If the EveDB array cell is not empty, the value is known by the attacker.

4.2. Remembering Complex Message Elements

Complex elements such as signatures and encryptions are stored by the intruder in additional channels which work like FIFO queues. While generating a faked message, needed elements are randomly chosen from channels. The exemplary usage was shown in Fig. 6 (right). Channel EveSig2 holds signs from the second protocol message, that were intercepted earlier.

4.3. Eavesdrop On Send, Corrupt on Receive Tactic

In a simple model the message is produced by the legal user, the intruder learns it and then the message is sent. Yet before the receiver gets it, the data is intercepted and generated once more by the attacker on the basis of their knowledge. An observation can be made, that it makes no sense to transport via the channels the data that is already stored in the intruder database. It can be seen that the original message is not used after the intruder learns it.
Therefore in our approach channels transport only information that a message is sent as shown in Fig. 7. In consequence, all channels memory usage size is constant and small. Thus the tactic is crucial for minimizing the model size.

It is also important that in our model the intruder can produce faked message after arbitrary time, possibly after receiving other messages from parallel protocol runs and learning new data. This models the ability of the intruder to delay messages. In Fig. 7, a circle is a point where a message is consumed by the attacker, while a square marks creation of a message by him. As can be seen message $M_1'$ is produced after learning message $M_2$ from the second protocol run. Sending of message is also the point where the intruder decides where the message will be sent.
Fig. 7. Eavesdrop on send, corrupt on receive schema and specification in the Promela language.

The effect is achieved by combining attackers’ activities with the users’ steps, rather than putting them into a separate process. As the method name suggests the intruder takes his first action (eavesdropping) just after the legal user sends a message. The instructions are put into the sender process. The attackers’ second action (corrupting the message) is put into receivers’ process, just before the legal user receives a message.

The tactic also eliminates introduction of additional mechanism to prevent the intruder from intercepting his own, faked messages. This could have been an additional field in a message, indicating if the message was sent by the attacker that can be found in the literature [10]. With the tactic this is not needed, as the data is generated only once before the legal user receives it.

4.4. Only One Channel For a Message

Using for every message two channels (first for transporting data from the legal user to the intruder, second for transporting data to the legal receiver) is simple and intuitive but memory expensive. Thus only one channel is used in our model. This can be done as no message data is really transported as was mentioned. Only information that a message is sent is placed in channels.

4.5. All Users in One Process

The eavesdrop on send, corrupt on receive technique also makes it possible to place the code of all users in one process. As it was mentioned, the intruders’ actions are combined with those of legal users. In a simple model senders and receivers could be put in independent processes. Every step of a user consists
of receiving and/or sending a message. Such step would be put into an **atomic** clause to minimize interleavings we are not interested in. This would result in a sequence of users’ atomic steps from different protocol runs, which is the models proper behaviour.

Yet the presence of many processes would cause the model checker to create an asynchronous product of the automata. This would introduce redundant interleaving and make the verified model grow too much [3]. That is why only one process is used with a do loop, in which from the set of executable steps one is nondeterministically chosen. Every step is represented by a function to keep both the advantage of one process and of having structured the Promela code, as shown in Fig. 7. Rather than storing the user identity in his process state, it becomes the function parameter. For example, a receiver of the message is indicated by the `self` parameter of function `recvMSG1sendMSG2()`.

In such a model a sequence of nondeterministically chosen steps is produced just as in the multi-process case but without the undesirable overhead. So this approach does not affect the models functionality but its efficiency.

### 4.6. Consistent Message Generation by the Intruder

The consistent generation of messages means that once chosen, an element (e.g. nonce, exponential) is used by the intruder in the whole message. This helps avoid messages that are known to be rejected by legal users. The example of this was shown in Fig. 6 (right). Here the same value is used as exponential of a responder `expr` in the plain text and in the faked signature.

### 5. Protocol Properties Verification

The last step is specifying protocol properties as **LTL** (linear temporal logic) formulas. Notation $\square \alpha$ means that $\alpha$ is satisfiable in the model, iff it is true for every execution path of the automata. It can be used to specify that it is desirable that unsafe states are never reachable. For the Needham–Schroeder protocol, an example safety formula detecting identity misbiding would be:

$$\alpha = \neg \square \alpha$$

$$=( \text{run2accepted} \land \text{>IDB} \land \text{otherUsrB[COMM1] == IDE}$$

$$\lor \text{otherUsrA[COMM1] == IDE} \land \text{otherUsrB[COMM1] == IDA})$$

The wrong state is when one of the legitimate users accepts a session with another legal user (IDA or IDB), while this user did not, because he was engaged in a run with the intruder (IDE). So it should hold that $M \models \square \neg \alpha$.

Fig. 8 presents a readable counterexample for the attack. It was automatically produced by a simple driver written by the authors, that runs the model checker, parses `Spins` output and interprets it. Only the emphasis was added by hand.
for more readability. The required information, from the raw output of the model checker, originates from the printing commands shown in Figs 1 and 6. Form the listing it can be seen that Bob accepted a session with Alice who never took part in a run with Bob.

Fig. 8. A description of an identity misbinding attack in the Needham–Schroeder protocol detected with Spin. Model checker raw output (left) and a readable output generated by our driver (right).

Another issue about writing formulas is the labels mechanism. It should be used if possible because it avoids additional, global variables to mark a state. Labels in Promela are inserted into code just as in C language. Expression of the form \((\text{ProcessName}@\text{LabelName})\) used in a formula, will discover a point where the process is in the labelled state.

As for the JFKi protocol the following two examplary formulas are presented.

\[
\gamma = (\text{JFKiProtocol}@\text{INTRUDER}.\text{DECRYPTED}.\text{MSG3}.\text{LABEL} \\
\&\& \text{cert} = \text{IDE})
\]

\[
\psi = (\text{JFKiProtocol}@\text{ACCEPTED}.\text{INIT}.\text{SA}.\text{LABEL} \&\& \text{cert} = \text{IDE} \\
\&\& \text{secAssos} = \text{SaiE})
\]

The first formula is used to detect privacy violation attack, when Eve decrypts the third message that was not supposed for her (global variable cert stores certificate of the peer chosen by the initiator and it is not IDE).
The second formula is true if the responder accepts a wrong initiators security association. That is when the association was inserted by the intruder (\textit{SaiE}), although it should originate from a legal initiator (whose identity is kept in \textit{cert}). There should never happen a situation when these formulas are true, so the \textit{B"uchi} automata are built for the formulas $\square \neg \gamma$ and $\square \neg \psi$. With analogical formulas, the ability of the adversary to change the exponentials, nonce and Diffie–Hellman group information can be checked. During the verification of \textit{JFKi} protocol with \textit{Spin}, none of the attacks was detected.

6. Application of the Method and Computational Results

The following model instance configuration was used for the verification results below: two parallel runs, two legal protocol users, intruder knowledge database containing two elements (\textit{Needham–Schroeder}) or one element (\textit{JFKi}) of every type of the complex element. In the second case, to give the adversary more abilities, any received complex element is stored in the databases nondeterministically. So the first element may not fill the queue completely. This configuration makes it feasible to verify a protocol on an average computer (AMD Athlon 2.01GHz, 2GB RAM) and lets expect the standard attacks to be detected.

Costs of example protocols verification are shown in Table 2. Our models are indicated bold. Sources of model from [10] are available, so scaled down to two parallel runs, they were included as a comparison. Also publicized fragments of [15] model give a hint of its size. At the first sight it is visible that the unminimized models present much bigger state vectors. In the case of \textit{JFKi}, it can be very distinctly seen how beneficial for verification were the reductions of the model. The original automaton was much too complex and was only partially analyzed. The minimized model could be verified in less than a quarter of hour. Protocol security properties did hold in the \textit{JFKi} model.

<table>
<thead>
<tr>
<th>Protocol</th>
<th>Time</th>
<th>Reached states</th>
<th>State vector</th>
<th>Used memory</th>
<th>Verification type</th>
</tr>
</thead>
<tbody>
<tr>
<td>Needham–Schroeder [10]</td>
<td>2770s</td>
<td>3.10e+007</td>
<td>224b</td>
<td>1128.758MB</td>
<td>partial</td>
</tr>
<tr>
<td>Needham–Schroeder [15]</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>code fragments</td>
</tr>
<tr>
<td>Needham–Schroeder</td>
<td>12.7s</td>
<td>3.06e+006</td>
<td>92b</td>
<td>98.094MB</td>
<td>full</td>
</tr>
<tr>
<td>JFKi non reduced</td>
<td>172s</td>
<td>4.00e+006</td>
<td>1916b</td>
<td>1851.350MB</td>
<td>partial</td>
</tr>
<tr>
<td>JFKi reduced</td>
<td>650s</td>
<td>3.62e+007</td>
<td>204b</td>
<td>1051.023MB</td>
<td>full</td>
</tr>
</tbody>
</table>

7. Conclusions and Future Plans

The proposed modelling framework has proved to be computationally efficient, enabling verification of more complex protocols. Although the approach
is more work consuming than using tools specialized only in verification of cryptog- 
ographic protocols such as Casper [21, 2], yet it gives more control over the 
model configuration. Another fact is that the discussed method is far more 
readable than for example CSP [2] or clauses for the ProVerif program [22]. 
Hence it is more accessible for the inspection and less error prone. In addition, 
automatic generation of counterexamples is a true asset of the method.

The presented framework is a proposal of a method complementary to the ex-
isting ones, being aimed at solving the difficult problem of assuring correctness 
of safety protocols.

As for the future improvements, designing a methodology to divide the model 
would make it possible to verify more parallel runs of a protocol. For example, 
in each part the initiator would choose a different responder. Analysis of each 
such model would require less memory and could be possibly done concurrently 
on separate computers, saving the time.

A more complex task would be to create a parser that would transform a pro-
tocol specification, in a protocol description language such as CAPSL [23], into 
a model. The Promela code could be still edited by the human verifier if needed 
but the main work would be done automatically. This offers another oppor-
tunity, that from the same input many outputs can be generated, including 
several verification models or a protocol implementation [23, 17].

References

[1] Uk chip and pin credit / debit cards are insecure (2009) 
http://www.youtube.com/watch?v=JPAX321gkrw
http://spinroot.com/spin/Doc/course/
http://anna.fi.muni.cz/models/cgi/model_info.cgi?name=needham
http://www.albertolluch.com/research/promelamodels
http://www.loria.fr/merz/papers/NeedhamSchroeder.spin
(2009).
Report, Microsoft Research (2009).
[18] Stamer H., Verification of cryptographic protocols, Technical Report, University of Kassel
(2005).
[19] Schnoebelen Ph., The complexity of temporal logic model checking, Advances in Modal
Logic (2003).
http://web.comlab.ox.ac.uk/people/Gavin.Lowe/Security/Casper/
[22] ProVerif: Cryptographic protocol verifier in the formal model
http://www.proverif.ens.fr/
Science Laboratory (1999).